

Board of Technical Education, Delhi

STATEMENT OF MARKS

(Exam. held in JAN 2017)

Roll No : 1402071006 Name : ABHISHEK TYAGI

Semester : V Electronic Engineering(Digital Electronics & Microprocessor. S

Name of Institution: Ambedkar Polytechnic, Shakarpur, Delhi

DUPLICATE

Subject Code	Subject Name	Marks		Marks Obtained	
		Max	Min	Theory	Practical
ED570	Communication System	150	60	69	--
ED571	Digital System Design	150	60	74	--
ED572	Troubleshooting and Maint. of Elec. Equ.	150	60	63	--
ED573	Microprocessor Based System Design	150	60	73	--
ED574	Object Oriented Prog. using C++	150	60	70	--
ED580	Communication System	100	40	--	84
ED581	Digital System Design	100	40	--	84
ED582	Troubleshooting and Maint. of Elec. Equ.	100	40	--	81
ED583	Microprocessor Based System Design	100	40	--	70
ED584	Object Oriented Prog. using C++	100	40	--	73
ED585	Industrial Training Report Presentation	150	60	--	125
Total				349	517
Grand Total				866/1400(Passed)	

Note: Astrisk indicates that candidate has failed to secure minimum pass marks in that subject
AA indicates absent for that examination

Checked By:

Place : Delhi
Dated : 11/03/25

S.P. Mehta

CONTROLLER

