



**JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD**  
HYDERABAD - 500 085, TELANGANA STATE, INDIA.

**CONSOLIDATED MARKS MEMO / CREDIT SHEET**

B.Tech. COMPUTER SCIENCE & ENGINEERING

CMM. No. : **C 0585553**

Serial No. : 21132016222

Name : **SANDHYA RAI**



Name of the College : M1-B S I T, GOLLAPALLY

Month & Year of Final Exam : May, 2015

Hall Ticket No. : **11M11A0590**

Year of Admission : 2011-2012

Class Awarded : **FIRST CLASS WITH DISTINCTION**

S.No.	SUBJECT TITLE	INT. MARKS	EXT. MARKS	TOTAL	CREDITS	S.No.	SUBJECT TITLE	INT. MARKS	EXT. MARKS	TOTAL	CREDITS
<b>I YEAR</b>											
1	ENGLISH	23	51	74	4	2	MATHEMATICS - I	23	29	52	6
3	MATHEMATICAL METHODS	24	28	52	6	4	ENGINEERING PHYSICS	22	39	61	4
5	ENGINEERING CHEMISTRY	23	40	63	4	6	COMPUTER PROGRAMMING & DATA STRUCTURES	24	41	65	6
7	ENGINEERING DRAWING	25	30	55	4	8	COMPUTER PROGRAMMING LAB	25	49	74	4
9	ENGG. PHYSICS & ENGG. CHEMISTRY LAB	25	50	75	4	10	ENGLISH LANGUAGE COMMUNICATION SKILLS LAB	25	50	75	4
11	IT WORKSHOP & ENGINEERING WORKSHOP	22	39	61	4						
<b>I SEMESTER</b>											
<b>I SEMESTER</b>						<b>II SEMESTER</b>					
1	PROBABILITY AND STATISTICS	22	43	65	3	1	COMPUTER ORGANIZATION	21	26	47*	4
2	MATHEMATICAL FOUNDATION OF COMPUTER SCIENCE	22	31	53	3	2	DATA BASE MANAGEMENT SYSTEMS	20	34	54	4
3	DATA STRUCTURES THROUGH C++	21	27	48	4	3	OBJECT ORIENTED PROGRAMMING	20	52	72	4
4	DIGITAL LOGIC DESIGN	21	26	47*	4	4	ENVIRONMENTAL STUDIES	23	34	57	3
5	ELECTRONIC DEVICES & CIRCUITS	23	26	49	4	5	FORMAL LANGUAGES & AUTOMATA THEORY	21	37	58	3
6	BASIC ELECTRICAL ENGG	21	58	79	3	6	DESIGN & ANALYSIS OF ALGORITHMS	22	39	61	3
7	ELECTRICAL AND ELECTRONICS LAB	24	49	73	2	7	OBJECT ORIENTED PROGRAMMING LAB	22	47	69	2
8	DATA STRUCTURES LAB THROUGH C++	23	50	73	2	8	DATA BASE MANAGEMENT SYSTEMS LAB	24	48	72	2
<b>II SEMESTER</b>											
<b>I SEMESTER</b>						<b>II SEMESTER</b>					
<b>III YEAR</b>											
1	PRINCIPLES OF PROGRAMMING LANGUAGES	25	52	77	4	1	MANAGERIAL ECONOMICS & FINANCIAL ANALYSIS	22	40	62	4
2	COMPUTER FORENSICS	21	53	74	3	2	ADVANCED ENGLISH COMMUNICATION SKILLS (LAB)	22	47	69	2
3	SOFTWARE ENGINEERING	22	65	87	3	3	NETWORK SECURITY	25	45	70	3
4	MICROPROCESSORS & INTERFACING	24	46	70	3	4	WEB TECHNOLOGIES & COMPILER DESIGN (LAB)	25	50	75	2
5	OPERATING SYSTEMS	24	59	83	4	5	VLSI DESIGN	25	51	76	3
6	DATA COMMUNICATION & COMPUTER NETWORKS	25	47	72	4	6	COMPILER DESIGN	24	53	77	4
7	MICROPROCESSORS & INTERFACING (LAB)	25	49	74	2	7	WEB TECHNOLOGIES	23	55	78	3
8	COMPUTER NETWORKS & OPERATING SYSTEMS (LAB)	25	50	75	2	8	OBJECT ORIENTED ANALYSIS AND DESIGN	25	54	79	4
<b>II SEMESTER</b>											
<b>I SEMESTER</b>						<b>II SEMESTER</b>					
<b>IV YEAR</b>											
1	DESIGN PATTERNS	23	40	63	3	1	MANAGEMENT SCIENCE	23	42	65	3
2	DATA WAREHOUSING AND DATA MINING	25	40	65	4	2	WEB SERVICES	25	53	78	3
3	MOBILE COMPUTING	24	42	66	3	3	ADHOC & SENSORS NETWORKS	25	40	65	3
4	LINUX PROGRAMMING	25	46	71	4	4	INDUSTRY ORIENTED MINI PROJECT	-	49	49	2
5	SOFTWARE TESTING METHODOLOGIES	25	48	73	4	5	SEMINAR	50	-	50	2
6	LINUX PROGRAMMING AND DATA MINING LAB	25	49	74	2	6	PROJECT WORK	50	150	200	10
7	CASE TOOLS AND SOFTWARE TESTING LAB	25	50	75	2	7	COMPREHENSIVE VIVA	-	99	99	2
8	COMPUTER GRAPHICS	25	54	79	3						
(# Project Internal=50, External=150)											

Number of Credits registered for : 200 Aggregate Marks Secured for best : 192

Aggregate Marks Secured : 3960 OUT OF 5250 (75.43%)

Date of Issue : June 14, 2015

(see overleaf for Rules concerned to award of class)

A indicates 'ABSENT'



(\*Courses registered but not counted for calculation of aggregate)

*Signature*

**CONTROLLER OF EXAMINATIONS**