



ವಿಶ್ವೇಶ್ವರಯ್ಯ ತಾಂತ್ರಿಕ ವಿಶ್ವವಿದ್ಯಾಲಯ, ಬೆಳಗಾವಿ
VISVESVARAIAH TECHNOLOGICAL UNIVERSITY, BELGAUM

KARNATAKA, INDIA

STATEMENT OF MARKS
BE Sixth Semester January 2007
Computer Science & Engineering

J
06 490534

Name of the Student : **ARPAN CHATTERJEE**

Father's / Mother's Name : **ASHISH KUNAR CHATTERJEE**

Name of the College : **VEERANAG INSTITUTE OF TECHNOLOGY, BANGALORE**

Seat No. : **1W0209006**

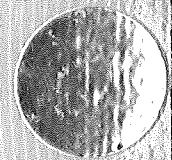
Sl. No.	Code	Subject	Examination Marks			Internal Assessment Marks		Total Marks		Subject Result		
			Max.	Min.	Obtained	Test	Att. Obtained	Max.	Min.		Obtained	
1	CS65	System Simulation and Modeling	100	35	35	25		18	125	50	55	Pass
GRAND TOTAL												

Total Marks Obtained (in words) : *********

Result of the Semester : **SECOND CLASS**

DATE : **APR 18, 2007**

A - Absent NE - Not Eligible



Registrar (Evaluation)

(Handwritten Signature)