

ವಿಶ್ವವಿದ್ಯಾರಣ್ಯ ತಾಂತ್ರಿಕ ವಿಶ್ವವಿದ್ಯಾಲಯ, ಬೆಳಗಾವಿ



VISVESWARAIAH TECHNOLOGICAL UNIVERSITY, BELGAUM

KARNATAKA, INDIA

J 225848
05

STATEMENT OF MARKS

BE Third Semester January 2005
Computer Science & Engineering

Name of the Student : ARPAN CHATTERJEE

Father's / Mother's Name : ASHISH KUMAR CHATTOPADHAYA

Seat No. : 1V103CS006

Name of the College : VEMANA INSTITUTE OF TECHNOLOGY, BANGALORE

Sl. No.	Subject		Examination Marks			Internal Assessment Marks			Total Marks		Subject Result	
	Code	Title	Max.	Min.	Obtained	Max.	Min.	Obtained	Max.	Min.		Obtained
1	MAT31	Engineering Mathematics - III	100	35	43	25	-	15	125	50	58	Pass
2	EC32	Electronics Circuits	100	35	35	25	-	25	125	50	60	Pass
3	CS33	Logic Design	100	35	48	25	-	24	125	50	72	Pass
4	CS34	Discrete Mathematical Structures	100	35	35	25	-	16	125	50	51	Pass
5	CS35	Data Structures with C	100	35	48	25	-	20	125	50	68	Pass
6	CS36	OOP with C++	100	35	45	25	-	20	125	50	65	Pass
7	CSL37	Data Structures Lab	50	20	40	25	12	15	75	32	55	Pass
8	CSL38	Analog & Digital Electronics Lab	50	20	24	25	12	23	75	32	47	Pass
GRAND TOTAL									900	364	476	

Total Marks Obtained (in words) : FOUR HUNDRED AND SEVENTY SIX ONLY

Result of the Semester : SECOND CLASS

DATE : JUN 23, 2005

A - Absent NE - Not Eligible



V. S. Reddy
Registrar (Evaluation)