



ವಿಶ್ವವಿದ್ಯಾರಣ್ಯ ತಾಂತ್ರಿಕ ವಿಶ್ವವಿದ್ಯಾಲಯ, ಬೆಳ್ಗಾವಿ

VISVESWARAIAH TECHNOLOGICAL UNIVERSITY, BELGAUM

KARNATAKA, INDIA

J 05 458185

STATEMENT OF MARKS

BE Fourth Semester July 2005

Computer Science & Engineering

Name of the Student : **ARPAN CHATTERJEE**

Father's / Mother's Name : **ASHISH KUMAR CHATTOPADHAYA**

Seat No. : **1V103CS006**

Name of the College : **VEMANA INSTITUTE OF TECHNOLOGY, BANGALORE**

Sl. No.	Code	Subject Title	Examination Marks		Internal Assessment Marks		Total Marks		Subject Result		
			Max.	Min.	Obtained	Max.	Min.	Obtained			
1	MAT41	Engineering Mathematics - IV	100	35	35	25	10	125	50	45	Fail
2	CS42	Graph Theory and Combinatorics	100	35	47	25	15	125	50	62	Pass
3	CS43	Analysis and Design of Algorithms	100	35	56	25	16	125	50	72	Pass
4	CS44	Finite Automata & Formal Languages	100	35	26	25	19	125	50	45	Fail
5	CS45	Introduction to Microprocessors	100	35	43	25	15	125	50	58	Pass
6	CS46	Computer Organisation	100	35	35	25	15	125	50	50	Pass
7	CSL47	OOP Lab with C++	50	20	43	25	22	75	32	65	Pass
8	CSL48	Algorithms Lab	50	20	48	25	23	75	32	71	Pass
GRAND TOTAL							900	364	468		

Total Marks Obtained (in words) : **FOUR HUNDRED AND SIXTY EIGHT ONLY**

Result of the Semester : **FAIL**

DATE : **NOV 24 2005**



V. S. S. S.
Registrar (Evaluation)