



ವಿಶ್ವೇಶ್ವರಯ್ಯ ತಾಂತ್ರಿಕ ವಿಶ್ವವಿದ್ಯಾಲಯ, ಬೆಳಗಾವಿ
VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELAGAVI

KARNATAKA, INDIA

STATEMENT OF MARKS

BE Third Semester January 2016

Computer Science & Engineering

J 16 144423



Name of the Student : S JABEZ DANIEL

Father's / Mother's Name : G R SILVANCE DANIEL

Seat No. : 1M/J14CS117

Name of the College : M.V. JAYARAMAN COLLEGE OF ENGINEERING, BANGALORE

Sl. No.	Code	Subject Title	Examination Marks			Internal Assessment Marks			Total Marks			Subject Result
			Max.	Min.	Obtained	Max.	Min.	Obtained	Max.	Min.	Obtained	
1	10MAT31	Engineering Mathematics-III	100	35	80	25	-	18	125	50	78	Pass
2	10CSS32	Electronic Circuits	100	35	45	25	-	18	125	50	63	Pass
3	10CSS33	Logic Design	100	35	65	25	-	18	125	50	83	Pass
4	10CSS34	Discrete Mathematical Structures	100	35	57	25	-	24	125	50	81	Pass
5	10CSS35	Data Structures with C	100	35	65	25	-	25	125	50	90	Pass
6	10CSS36	Object Oriented Programming with C++	100	35	63	25	-	23	125	50	86	Pass
7	10CSL37	Data Structures with C/C++ Lab	50	20	42	25	12	24	75	32	66	Pass
8	10CSL38	Electronic Circuits & Logic Design Lab	50	20	39	25	12	23	75	32	62	Pass
GRAND TOTAL			900	364	609							

Total Marks Obtained (in words) : SIX HUNDRED AND NINE ONLY

Result of the Semester : FIRST CLASS

DATE : JUL 25, 2016

A - Absent NE - Not Eligible



Registrar (Evaluation)