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# JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY



HYDERABAD, ANDHRA PRADESH, INDIA

College Code: L7 ( P C E T, GHATKESAR )

*Mr. R Vihar*

*S/o R. Om Prakash*

having fulfilled the academic requirements and passed the examination held during *November - 2006* in *\*\* Second Class \*\** has this day been admitted by the Executive Council to the Degree of

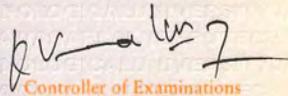
*Bachelor of Technology*  
( *Electrical & Electronics Engineering* )

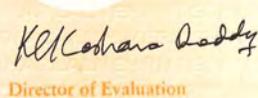
Given under the Seal of the University

H.T.No : **02L71A0240**

0612476

Date : 30 March, 2007

  
Controller of Examinations

  
Director of Evaluation

  
Registrar



**JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY**  
HYDERABAD - 500 072.  
ANDHRA PRADESH, INDIA



**CONSOLIDATED MARKS MEMO / CREDIT SHEET**

CMM. No: A 104737

Bachelor of Technology in **ELECTRICAL & ELECTRONICS ENGINEERING**

Serial No.: 559484094237  
Name: R VIHAR  
Hall Ticket No.: 02L71A0240

Year of Admission 2002-2003

Name of the College: P. C. E. T. GHATKESAR  
Month & Year of Final Exam: November, 2006  
Class Awarded: \*\*\* SECOND CLASS \*\*\*

S.No.	COURSE TITLE	INTERNAL MARKS	EXTERNAL MARKS	TOTAL	CREDITS	S.No.	COURSE TITLE	INTERNAL MARKS	EXTERNAL MARKS	TOTAL	CREDITS
	THEORY Max.	20	80	100			THEORY Max.	20	80	100	
	LAB	25	50	75			LAB	25	50	75	

**I YEAR**

1	ENGLISH	15	30	45	6	2	MATHEMATICS - I	14	39	53	6
3	SOLID STATE PHYSICS	12	46	58	4	4	INFORMATION TECH. & NUME. METHODS	16	30	46	6
5	C & DATA STRUCTURES	12	28	40	6	6	NETWORK THEORY	10	47	57	6
7	ELECTRONICS DEVICES & CIRCUITS	10	44	54	6	8	ENGINEERING DRAWING PRACTICE	18	28	46	4
9	COMPUTER PROGRAMMING (LAB)	20	33	53	8	10	ELECTRONICS DEVICES & CKTS (LAB)	12	20	32	4

**I SEMESTER**

**II YEAR**

**II SEMESTER**

1	MATHEMATICS - II	14	36	50	4	1	MANAGERIAL ECONOMICS & FINANCIAL A	14	29	43	4
2	HYDRAULICS & HYDRAULIC MACHINERY	13	41	54	4	2	MATHEMATICS - III	10	45	55	4
3	PULSE & DIGITAL CIRCUITS	12	35	47	4	3	LINEAR & DIGITAL IC APPLICATIONS	16	28	44	4
4	SWITCHING THEORY & LOGIC DESIGN	14	57	71	4	4	POWER SYSTEMS - I	10	30	40	4
5	ELECTROMAGNETIC FIELDS	12	28	40	4	5	ELECTROMECHANICS - II	8	32	40	4
6	ELECTRO MECHANICS - I	12	32	44	4	6	CONTROL SYSTEMS	16	28	44	4
7	HYDRAULICS & HYDRAULIC MACHINERY (	20	42	62	2	7	IC & PULSE & DIGITAL CIRCUITS (LAB)	15	30	45	2
8	NETWORKS (LAB)	24	42	66	2	8	ELECTROMECHANICS (LAB) - I	15	30	45	2

**I SEMESTER**

**III YEAR**

**II SEMESTER**

1	COMPUTER ORGANIZATION	15	28	43	4	1	ANALYSIS OF LINEAR SYSTEMS	12	28	40	4
2	ELECTRICAL MEASUREMENTS	14	28	42	4	2	MICROPROCESSORS AND INTERFACING	14	28	42	4
3	POWER SYSTEMS-I	14	28	42	4	3	MODELLING OF POWER SYSTEM COMPONENTS	11	29	40	4
4	POWER ELECTRONICS	12	36	48	4	4	INSTRUMENTATION	15	28	43	4
5	ELECTROMECHANICS-III	10	34	44	4	5	POWER SYSTEMS - III	12	29	41	4
6	MANAGEMENT SCIENCE	14	30	44	4	6	UTILIZATION OF ELECTRICAL ENERGY	15	37	52	4
7	ELECTROMECHANICS (LAB) - II	21	36	57	2	7	ELECTRICAL MEASUREMENTS LAB	17	40	57	2
8	CONTROL SYSTEMS (LAB)	20	34	54	2	8	POWER ELECTRONICS LAB	15	44	59	2

**I SEMESTER**

**IV YEAR**

**II SEMESTER**

1	DIGITAL SIGNAL PROCESSING	14	31	45	4	1	H V D C TRANSMISSION	18	47	65	4
2	POWER SEMICONDUCTOR DRIVES	15	28	43	4	2	DATABASE MANAGEMENT SYSTEMS	11	31	42	4
3	COMPUTER METHODS IN POWER SYS.	15	35	50	4	3	PROJECT & SEMINAR #	32	150	182	8
4	POWER SYSTEM OPERATION & CONTROL	16	29	45	4						
5	NON-CONVENTIONAL ENERGY SOURCES	15	30	45	4						
6	ELECTRICAL DISTRIBUTION SYS.	16	28	44	4						
7	MICROPROCESSORS LAB	23	41	64	2						
8	SIMULATION OF ELEC. SYS. LAB	21	41	62	2						

Number of Credits registered for: 212 (# Project Internal = 40, External = 160)

Aggregate Marks Secured for best 212 credits: 2709 OUT OF 5075 (53.38 %)

Date of Declaration of Result: December 16, 2006

(See overleaf for instruments)

(\*Courses registered for but not counted for calculation of aggregate)

*P. C. E. T. Ghatkesar*  
CONTROLLER OF EXAMINATIONS