



ವಿಶ್ವೇಶ್ವರಯ್ಯ ತಾಂತ್ರಿಕ ವಿಶ್ವವಿದ್ಯಾಲಯ, ಬೆಳಗಾವಿ
VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELGAUM

KARNATAKA, INDIA

STATEMENT OF MARKS

BE First Semester January 2012
 Electronics & Communication Engineering



J 12 129909

Name of the Student : PRABHAKAR.P.V

Father's / Mother's Name : VENKATANARAYANA

Seat No. : 1VA11EC062

Name of the College : SAI VIDYA INSTITUTE OF TECHNOLOGY, BANGALORE

Sl. No.	Subject		Examination Marks			Internal Assessment Marks			Total Marks			Subject Result
	Code	Title	Max.	Min.	Obtained	Max.	Min.	Obtained	Max.	Min.	Obtained	
1	10MAT11	Engineering Mathematics - I	100	35	37	25	-	16	125	50	53	Pass
2	10CHE12	Engineering Chemistry	100	35	45	25	-	17	125	50	62	Pass
3	10CCP13	Computer Concepts & C Programming	100	35	55	25	-	19	125	50	74	Pass
4	10CED14	Computer Aided Engineering Drawing	100	35	57	25	-	18	125	50	75	Pass
5	10ELN15	Basic Electronics	100	35	39	25	-	15	125	50	54	Pass
6	10CPL16	Computer Programming Lab	50	20	34	25	12	17	75	32	51	Pass
7	10CHEL17	Engineering Chemistry Lab	50	20	29	25	12	18	75	32	47	Pass
8	10CIV18	Environmental Studies	50	18	35	25	-	25	75	30	60	Pass
GRAND TOTAL									775	314	416	

Total Marks Obtained (in words) : FOUR HUNDRED AND SIXTEEN ONLY

Result of the Semester : SECOND CLASS

DATE : JUN 27, 2012

06/10/CIP/CIV18 is not considered for Grand Total and the Class Declaration

A - Absent NE - Not Eligible



Registrar (Evaluation)



ವಿಶ್ವವರಯ ತಾಂತ್ರಿಕ ವಿಶ್ವವಿದ್ಯಾಲಯ, ಬೆಳಗಾವಿ



VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELGAUM

KARNATAKA, INDIA

STATEMENT OF MARKS

BE Second Semester July 2012
Electronics & Communication Engineering

J 299692
12

Name of the Student : PRABHAKAR.P.V

Father's / Mother's Name : VENKATANARAYANA

Seat No. : 1VA11EC062

Name of the College : SAI VIDYA INSTITUTE OF TECHNOLOGY, BANGALORE

Sl. No.	Subject		Examination Marks			Internal Assessment Marks			Total Marks			Subject Result
	Code	Title	Max.	Min.	Obtained	Max.	Min.	Obtained	Max.	Min.	Obtained	
1	10MAT21	Engineering Mathematics - II	100	35	22	25	-	15	125	50	37	Fail
2	10PHY22	Engineering Physics	100	35	35	25	-	15	125	50	50	Pass
3	10CIV23	Elements of Civil Engg. & Engg. Mechanics	100	35	41	25	-	16	125	50	57	Pass
4	10EME24	Elements of Mechanical Engineering	100	35	54	25	-	22	125	50	76	Pass
5	10ELE25	Basic Electrical Engineering	100	35	47	25	-	16	125	50	63	Pass
6	10WSL26	Workshop Practice	50	20	43	25	12	25	75	32	68	Pass
7	10PHYL27	Engineering Physics Lab	50	20	32	25	12	16	75	32	48	Pass
8	10CIP28	Constitution of India & Professional Ethics	50	18	24	25	-	20	75	30	44	Pass
GRAND TOTAL									775	314	399	

Total Marks Obtained (in words) : THREE HUNDRED AND NINETY NINE ONLY

Result of the Semester : FAIL

DATE : OCT 11, 2012

06/10/CIP/CIV/28 is not considered for Grand Total and the Class Declaration

A - Absent NE - Not Eligible



(Signature)

Registrar (Evaluation)



ವಿಶ್ವೇಶ್ವರಯ್ಯ ತಾಂತ್ರಿಕ ವಿಶ್ವವಿದ್ಯಾಲಯ, ಬೆಳಗಾವಿ

VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELGAUM

KARNATAKA, INDIA

STATEMENT OF MARKS

BE Second Semester January 2013
Electronics & Communication Engineering



J 103218
13

Name of the Student : PRABHAKAR.P.V

Father's / Mother's Name : VENKATANARAYANA

Seat No. : 1VA11EC062

Name of the College : SAI VIDYA INSTITUTE OF TECHNOLOGY, BANGALORE

Sl. No.	Subject		Examination Marks			Internal Assessment Marks			Total Marks			Subject Result
	Code	Title	Max.	Min.	Obtained	Max.	Min.	Obtained	Max.	Min.	Obtained	
1	10MAT21	Engineering Mathematics - II	100	35	35	25	-	15	125	50	50	Pass
GRAND TOTAL									***	***	***	

Total Marks Obtained (in words) : *****

Result of the Semester : SECOND CLASS

DATE : MAY 03, 2013

A - Absent NE - Not Eligible

Registrar (Evaluation)



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VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELGAUM

KARNATAKA, INDIA

J 674815
12

STATEMENT OF MARKS
BE Third Semester January 2013
Electronics & Communication Engineering

Name of the Student : PRABHAKAR.P.V

Father's / Mother's Name : VENKATANARAYANA

Seat No. : 1VA11EC062

Name of the College : SAI VIDYA INSTITUTE OF TECHNOLOGY, BANGALORE

Sl. No.	Subject		Examination Marks			Internal Assessment Marks			Total Marks			Subject Result
	Code	Title	Max.	Min.	Obtained	Max.	Min.	Obtained	Max.	Min.	Obtained	
1	10MAT31	Engineering Mathematics-III	100	35	47	25	-	17	125	50	64	Pass
2	10ES32	Analog Electronic Circuits	100	35	22	25	-	15	125	50	37	Fail
3	10ES33	Logic Design	100	35	35	25	-	15	125	50	50	Pass
4	10ES34	Network Analysis	100	35	22	25	-	15	125	50	37	Fail
5	10IT35	Electronic Instrumentation	100	35	38	25	-	19	125	50	57	Pass
6	10ES36	Field Theory	100	35	26	25	-	12	125	50	38	Fail
7	10ESL37	Analog Electronics Lab	50	20	29	25	12	17	75	32	46	Pass
8	10ESL38	Logic Design Lab	50	20	24	25	12	21	75	32	45	Pass
GRAND TOTAL									900	364	374	

Total Marks Obtained (in words) : THREE HUNDRED AND SEVENTY FOUR ONLY

Result of the Semester : FAIL

DATE : APR 30, 2013

A - Absent NE - Not Eligible



(Signature)

Registrar (Evaluation)



ವಿಶ್ವೇಶ್ವರಯ್ಯ ತಾಂತ್ರಿಕ ವಿಶ್ವವಿದ್ಯಾಲಯ, ಬೆಳಗಾವಿ
VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELGAUM

KARNATAKA, INDIA

STATEMENT OF MARKS

BE Third Semester July 2013
 Electronics & Communication Engineering



J
13 459931

Name of the Student : PRABHAKAR.P.V

Father's / Mother's Name : VENKATANARAYANA

Seat No. : 1VA11EC062

Name of the College : SAI VIDYA INSTITUTE OF TECHNOLOGY, BANGALORE

Sl. No.	Subject		Examination Marks			Internal Assessment Marks			Total Marks			Subject Result
	Code	Title	Max.	Min.	Obtained	Max.	Min.	Obtained	Max.	Min.	Obtained	
1	10ES32	Analog Electronic Circuits	100	35	38	25	-	15	125	50	53	Pass
2	10ES34	Network Analysis	100	35	20	25	-	15	125	50	35	Fail
GRAND TOTAL									***	***	***	

Total Marks Obtained (in words) : *****

Result of the Semester : FAIL

DATE : OCT 04, 2013

A - Absent NE - Not Eligible



[Signature]

Registrar (Evaluation)



ವಿಶ್ವೇಶ್ವರಯ್ಯ ತಾಂತ್ರಿಕ ವಿಶ್ವವಿದ್ಯಾಲಯ, ಬೆಳಗಾವಿ
VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELGAUM

KARNATAKA, INDIA

STATEMENT OF MARKS

BE Third Semester January 2014
 Electronics & Communication Engineering

J
14 166247

Name of the Student : PRABHAKAR.P.V

Father's / Mother's Name : VENKATANARAYANA

Seat No. : 1VA11EC062

Name of the College : SAI VIDYA INSTITUTE OF TECHNOLOGY, BANGALORE

Sl. No.	Subject		Examination Marks			Internal Assessment Marks			Total Marks			Subject Result
	Code	Title	Max.	Min.	Obtained	Max.	Min.	Obtained	Max.	Min.	Obtained	
1	10ES34	Network Analysis	100	35	35	25	-	15	125	50	50	Pass
2	10ES36	Field Theory	100	35	38	25	-	12	125	50	50	Pass
GRAND TOTAL											***	***

Total Marks Obtained (in words) : *****

Result of the Semester : SECOND CLASS

DATE : APR 08, 2014

A - Absent NE - Not Eligible



Registrar (Evaluation)



ವಿಶ್ವವಿದ್ಯಾರಣ್ಯ ತಾಂತ್ರಿಕ ವಿಶ್ವವಿದ್ಯಾಲಯ, ಬೆಳಗಾವಿ



VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELGAUM

KARNATAKA, INDIA

J 493499
13

STATEMENT OF MARKS

BE Fourth Semester July 2013

Electronics & Communication Engineering

Name of the Student : PRIABHAKAR.P.V

Father's / Mother's Name : VENKATANARAYANA

Seat No. : 1VA11EC062

Name of the College : SAI VIDYA INSTITUTE OF TECHNOLOGY, BANGALORE

Sl. No.	Subject		Examination Marks			Internal Assessment Marks			Total Marks			Subject Result
	Code	Title	Max.	Min.	Obtained	Max.	Min.	Obtained	Max.	Min.	Obtained	
1	10MAT41	Engineering Mathematics - IV	100	35	36	25	-	20	125	50	56	Pass
2	10ES42	Microcontrollers	100	35	44	25	-	21	125	50	65	Pass
3	10ES43	Control Systems	100	35	52	25	-	15	125	50	67	Pass
4	10EC44	Signals & Systems	100	35	35	25	-	15	125	50	50	Pass
5	10EC45	Fundamentals of HDL	100	35	35	25	-	15	125	50	50	Pass
6	10EC46	Linear ICs & Applications	100	35	35	25	-	15	125	50	50	Pass
7	10ESL47	Microcontrollers Lab	50	20	30	25	12	15	75	32	45	Pass
8	10ECL48	HDL Lab	50	20	30	25	12	15	75	32	45	Pass
GRAND TOTAL									900	364	428	

Total Marks Obtained (in words) : FOUR HUNDRED AND TWENTY EIGHT ONLY

Result of the Semester : SECOND CLASS

DATE : OCT 06, 2013

A - Absent NE - Not Eligible

Registrar (Evaluation)



ವಿಶ್ವೇಶ್ವರಯ್ಯ ತಾಂತ್ರಿಕ ವಿಶ್ವವಿದ್ಯಾಲಯ, ಬೆಳಗಾವಿ



VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELGAUM

KARNATAKA, INDIA

STATEMENT OF MARKS

BE Fifth Semester January 2014

Electronics & Communication Engineering

J 001770
14

Name of the Student : PRABHAKAR.P.V

Father's / Mother's Name : VENKATANARAYANA

Seat No. : 1VA11EC062

Name of the College : SAI VIDYA INSTITUTE OF TECHNOLOGY, BANGALORE

Sl. No.	Subject		Examination Marks			Internal Assessment Marks			Total Marks			Subject Result
	Code	Title	Max.	Min.	Obtained	Max.	Min.	Obtained	Max.	Min.	Obtained	
1	10AL51	Management & Entrepreneurship	100	35	60	25	-	20	125	50	80	Pass
2	10EC52	Digital Signal Processing	100	35	40	25	-	10	125	50	50	Pass
3	10EC53	Analog Communication	100	35	36	25	-	25	125	50	61	Pass
4	10EC54	Microwaves & Radar	100	35	54	25	-	24	125	50	78	Pass
5	10EC55	Information Theory & Coding	100	35	54	25	-	16	125	50	70	Pass
6	10EC56	Fundamentals of CMOS VLSI	100	35	51	25	-	17	125	50	68	Pass
7	10ECL57	DSP Lab.	50	20	36	25	12	23	75	32	59	Pass
8	10ECL58	Analog Communication Lab. + LIC Lab.	50	20	40	25	12	22	75	32	62	Pass
GRAND TOTAL									900	364	528	

Total Marks Obtained (in words) : FIVE HUNDRED AND TWENTY EIGHT ONLY

Result of the Semester : SECOND CLASS

DATE : MAR 25, 2014

A - Absent NE - Not Eligible



[Signature]

Registrar (Evaluation)



ವಿಶ್ವೇಶ್ವರಯ್ಯ ತಾಂತ್ರಿಕ ವಿಶ್ವವಿದ್ಯಾಲಯ, ಬೆಳಗಾವಿ



VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELGAUM

KARNATAKA, INDIA

STATEMENT OF MARKS

BE Sixth Semester July 2014

Electronics & Communication Engineering

J
14 423647

Name of the Student : PRABHAKAR.P.V

Father's / Mother's Name : VENKATANARAYANA

Seat No. : 1VA11EC062

Name of the College : SAI VIDYA INSTITUTE OF TECHNOLOGY, BANGALORE

Sl. No.	Subject		Examination Marks			Internal Assessment Marks			Total Marks			Subject Result
	Code	Title	Max.	Min.	Obtained	Max.	Min.	Obtained	Max.	Min.	Obtained	
1	10EC61	Digital Communication	100	35	41	25	-	19	125	50	60	Pass
2	10EC62	Microprocessors	100	35	35	25	-	15	125	50	50	Pass
3	10EC63	Microelectronics Circuits	100	35	27	25	-	20	125	50	47	Fail
4	10EC64	Antennas & Propagation	100	35	39	25	-	15	125	50	54	Pass
5	10EC65	Operating Systems	100	35	50	25	-	21	125	50	71	Pass
6	10EC66	Programming in C++	100	35	45	25	-	17	125	50	62	Pass
7	10ECL67	Advanced Communication Lab.	50	20	43	25	12	24	75	32	67	Pass
8	10ECL68	Microprocessor Lab.	50	20	20	25	12	16	75	32	36	Pass
GRAND TOTAL									900	364	447	

Total Marks Obtained (in words) : FOUR HUNDRED AND FORTY SEVEN ONLY

Result of the Semester : FAIL

DATE : SEP 17, 2014

A - Absent NE - Not Eligible



[Signature]

Registrar (Evaluation)



ವಿಶ್ವೇಶ್ವರಯ್ಯ ತಾಂತ್ರಿಕ ವಿಶ್ವವಿದ್ಯಾಲಯ, ಬೆಳಗಾವಿ
VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELAGAVI

KARNATAKA, INDIA

STATEMENT OF MARKS

BE Sixth Semester January 2016
Electronics & Communication Engineering



J
16 219848

Name of the Student : PRABHAKAR.P.V

Father's / Mother's Name : VENKATANARAYANA

Seat No. : 1VA11EC062

Name of the College : SAI VIDYA INSTITUTE OF TECHNOLOGY, BANGALORE

Sl. No.	Subject		Examination Marks			Internal Assessment Marks			Total Marks			Subject Result
	Code	Title	Max.	Min.	Obtained	Max.	Min.	Obtained	Max.	Min.	Obtained	
1	10EC63	Microelectronics Circuits	100	35	41	25	-	20	125	50	61	Pass
GRAND TOTAL											***	***

Total Marks Obtained (in words) : *****

Result of the Semester : SECOND CLASS

DATE : JUL 26, 2016

A - Absent NE - Not Eligible



[Signature]

Registrar (Evaluation)



ವಿಶ್ವೇಶ್ವರಯ್ಯ ತಾಂತ್ರಿಕ ವಿಶ್ವವಿದ್ಯಾಲಯ, ಬೆಳಗಾವಿ



VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELAGAVI

KARNATAKA, INDIA

STATEMENT OF MARKS

BE Seventh Semester January 2015

Electronics & Communication Engineering

J 028585
15

Name of the Student : PRABHAKAR.P.V

Father's / Mother's Name : VENKATANARAYANA

Seat No. : 1VA11EC062

Name of the College : SAI VIDYA INSTITUTE OF TECHNOLOGY, BANGALORE

Sl. No.	Subject		Examination Marks			Internal Assessment Marks			Total Marks			Subject Result
	Code	Title	Max.	Min.	Obtained	Max.	Min.	Obtained	Max.	Min.	Obtained	
1	10EC71	Computer Communication Networks	100	35	39	25	-	24	125	50	63	Pass
2	10EC72	Optical Fiber Communication	100	35	47	25	-	19	125	50	66	Pass
3	10EC73	Power Electronics	100	35	35	25	-	20	125	50	55	Pass
4	10EC74	Embedded System Design	100	35	51	25	-	19	125	50	70	Pass
5	10EC752	Micro & Smart Systems Technology	100	35	63	25	-	18	125	50	81	Pass
6	10EC762	Real Time Systems	100	35	65	25	-	16	125	50	81	Pass
7	10ECL77	VLSI Lab.	50	20	28	25	12	17	75	32	45	Pass
8	10ECL78	Power Electronics Lab.	50	20	46	25	12	24	75	32	70	Pass
GRAND TOTAL									900	364	531	

Total Marks Obtained (in words) : FIVE HUNDRED AND THIRTY ONE ONLY

Result of the Semester : SECOND CLASS

DATE: APR 12 2015

A - Absent NE - Not Eligible

Registrar (Evaluation)



ವಿಶ್ವೇಶ್ವರಯ್ಯ ತಾಂತ್ರಿಕ ವಿಶ್ವವಿದ್ಯಾಲಯ, ಬೆಳಗಾವಿ
VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELAGAVI

KARNATAKA, INDIA

STATEMENT OF MARKS

BE Eighth Semester (Electronics & Communication Engineering)



F₁₆ 003261

Name of the Student : **PRABHAKAR.P.V**

Father's / Mother's Name : **VENKATANARAYANA**

Seat No. : **1VA11EC062**

Name of the College : **SAI VIDYA INSTITUTE OF TECHNOLOGY, BANGALORE**

Sl. No.	Subject		Examination Marks			Internal Assessment Marks			Total Marks			Month & Year of Passing
	Code	Title	Max.	Min.	Obtained	Max.	Min.	Obtained	Max.	Min.	Obtained	
1	10EC81	Wireless Communication	100	35	54	25	-	19	125	50	73	Jul 15
2	10EC82	Digital Switching Systems	100	35	42	25	-	15	125	50	57	Jul 15
3	10EC832	Network Security	100	35	44	25	-	20	125	50	64	Jul 15
4	10EC843	GSM	100	35	50	25	-	21	125	50	71	Jul 15
5	10EC85	Project Work	100	40	89	100	50	95	200	90	184	Jul 15
6	10EC86	Seminar	-	-	-	50	20	42	50	20	42	Jul 15

*First Attempt Total (in words) : **FOUR HUNDRED AND NINETY ONE ONLY**

GRAND TOTAL 750 310 491 *

Result of the Semester : **FIRST CLASS**

Results of V to VIII Semesters :

Semesters	V	VI**	VII	VIII	V TO VIII
Marks at First Attempt	528	447	531	491	1997
Total Marks (Max.)	900	900	900	750	3450
Result	SC	SC	SC	FC	SC

First Attempt Total (V to VIII Sem) in words : **ONE THOUSAND NINE HUNDRED AND NINETY SEVEN ONLY**

Result of the Degree : **SECOND CLASS**

Date : **AUG 08, 2016**

** Cleared during Jan 2016 exam



Registrar (Evaluation)