



JAWAHARLAL NEHRUTECHNOLOGICAL UNIVERSITY HYDERABAD
HYDERABAD - 500 085, TELANGANA STATE, INDIA.

CONSOLIDATED MEMO OF MARKS/GRADES AND CREDITS



C 5D 1 1 1 1 1 B.Tech. ELECTRONICS & COMMUNICATION ENGINEERING

Name : NALLAVELLI MAHENDHAR

Hall Ticket No : 16P81A0451

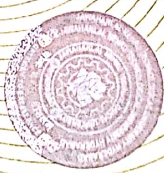
Serial No. : 21472000037

Year of Admission : 2016-2017

Name of the College : P8-JIET, CHINTHAPALLIGUDA

Month & Year of Final Exam : March, 2021

Class Awarded : SECOND CLASS



S.No.	SUBJECT TITLE	GRADE POINT	GRADE	CREDITS	S.No.	SUBJECT TITLE	GRADE POINT	GRADE	CREDITS
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I SEMESTER					II SEMESTER				
1	ENGINEERING MECHANICS	5	C	3	1	MATHEMATICS - II	0	F	0*
2	ENGINEERING CHEMISTRY	5	C	4	2	COMPUTER PROGRAMMING IN C	5	C	3
3	BASIC ELECTRICAL AND ELECTRONICS ENGINEERING	5	C	4	3	COMPUTER PROGRAMMING IN C LAB	6	B	2
4	MATHEMATICS - I	5	C	3	4	ENGINEERING PHYSICS - II	5	C	3
5	PROFESSIONAL COMMUNICATION IN ENGLISH	8	A	3	5	MATHEMATICS - III	5	C	4
6	ENGINEERING PHYSICS - I	6	B	3	6	ENGINEERING CHEMISTRY LAB	8	A	2
7	ENGINEERING WORKSHOP	7	B+	2	7	ENGINEERING GRAPHICS	5	C	4
8	ENGLISH LANGUAGE COMMUNICATION SKILLS LAB	10	O	2	8	ENGINEERING PHYSICS LAB	8	A	2

I SEMESTER					II SEMESTER				
1	NETWORK ANALYSIS	5	C	3	1	CONTROL SYSTEMS	5	C	4
2	ELECTRICAL TECHNOLOGY	5	C	4	2	ANALOG COMMUNICATIONS	5	C	4
3	ANALOG ELECTRONICS	5	C	4	3	PULSE AND DIGITAL CIRCUITS	5	C	4
4	MATHEMATICS - IV	5	C	4	4	BUSINESS ECONOMICS AND FINANCIAL ANALYSIS	5	C	3
5	SIGNALS AND STOCHASTIC PROCESS	5	C	3	5	ANALOG ELECTRONICS LAB	9	A+	2
6	BASIC ELECTRICAL ENGINEERING LAB	10	O	2	6	SWITCHING THEORY AND LOGIC DESIGN	5	C	3
7	ELECTRONIC DEVICES AND CIRCUITS LAB	8	A	2	7	ANALOG COMMUNICATIONS LAB	9	A+	2
8	BASIC SIMULATION LAB	10	O	2	8	PULSE AND DIGITAL CIRCUITS LAB	9	A+	2
9	ENVIRONMENTAL SCIENCE AND TECHNOLOGY A	-	-	0	9	GENDER SENSITIZATION LAB A	-	-	0

I SEMESTER					III YEAR					II SEMESTER				
1	ELECTROMAGNETIC THEORY AND TRANSMISSION LINES	5	C	4	1	MICROPROCESSORS AND MICROCONTROLLERS	5	C	4	1	SYSTEM DESIGN USING FPGAS	5	C	3
2	DIGITAL COMMUNICATIONS	5	C	4	2	ANTENNAS AND WAVE PROPAGATION	5	C	4	2	GLOBAL POSITIONING SYSTEM	6	B	3
3	LINEAR AND DIGITAL IC APPLICATIONS	5	C	4	3	DIGITAL SIGNAL PROCESSING	6	B	4	3	MAJOR PROJECT	9	A+	15
4	OPERATING SYSTEMS	5	C	3	4	DIGITAL SYSTEM DESIGN	6	B	3	4	ENTREPRENEURSHIP AND SMALL BUSINESS ENTERPRISE	7	B+	3
5	FUNDAMENTALS OF MANAGEMENT	6	B	3	5	JAVA PROGRAMMING	5	C	3					
6	DIGITAL COMMUNICATIONS LAB	10	O	2	6	MICROPROCESSORS AND MICROCONTROLLERS LAB	10	O	2					
7	LINEAR IC APPLICATIONS LAB	9	A+	2	7	ADVANCED ENGLISH COMMUNICATION SKILLS LAB	10	O	2					
8	DIGITAL IC APPLICATIONS LAB	9	A+	2	8	DIGITAL SIGNAL PROCESSING LAB	10	O	2					
9	PROFESSIONAL ETHICS A	-	-	0										

I SEMESTER					IV YEAR					II SEMESTER				
1	INDUSTRY ORIENTED MINI PROJECT	9	A+	2	1	SYSTEM DESIGN USING FPGAS	5	C	3	1	SYSTEM DESIGN USING FPGAS	5	C	3
2	ELECTRONIC MEASUREMENTS AND INSTRUMENTATION	5	C	3	2	GLOBAL POSITIONING SYSTEM	6	B	3	2	GLOBAL POSITIONING SYSTEM	6	B	3
3	VLSI DESIGN	6	B	4	3	MAJOR PROJECT	9	A+	15	3	MAJOR PROJECT	9	A+	15
4	EMBEDDED SYSTEM DESIGN	7	B+	3	4	ENTREPRENEURSHIP AND SMALL BUSINESS ENTERPRISE	7	B+	3	4	ENTREPRENEURSHIP AND SMALL BUSINESS ENTERPRISE	7	B+	3
5	MICROWAVE ENGINEERING	5	C	4										
6	COMPUTER NETWORKS	6	B	3										
7	MICROWAVE ENGINEERING LAB	9	A+	2										
8	VLSI AND E-CAD LAB	7	B+	2										
9	SEMINAR	9	A+	1										

Number of Credits registered for : 192

Aggregate Credits Secured for best : 188

Aggregate Marks/CGPA Secured : 6.39

Date of Issue : September 23, 2021

(See attached for rules concerning to award of class)
Ab indicates 'ABSENT'

(Mandatory course passed for award of degree)
(Courses registered but not coursed for calculation of aggregate)



K. Venkateshwarulu
CONTROLLER OF EXAMINATIONS