

ANNA UNIVERSITY, CHENNAI - 600 025



PROVISIONAL CERTIFICATE

Folio No.: **2514471**

This is to certify that the undermentioned candidate has qualified for the award of Degree through:

BANNARI AMMAN INSTITUTE OF TECHNOLOGY, ERODE

an autonomous college affiliated to Anna University as detailed below:

Name : **KANISHMA J**

Register No. : **7376211EC178**

Degree : **B.E.**

Branch/Specialization : **ELECTRONICS AND COMMUNICATION ENGINEERING**

Month & Year of Passing : **APRIL 2025**

Classification : **FIRST CLASS**



Chennai - 600 025

Date: 11-AUG-25

P. Sathish
Controller of Examinations

i/c



BANNARI AMMAN INSTITUTE OF TECHNOLOGY

(An Autonomous Institution Affiliated to Anna University, Chennai)

SATHYAMANGALAM - 638 401 TAMIL NADU INDIA

CONSOLIDATED STATEMENT OF GRADES

Folio No.: CA 040022



Sem.	Course Code	Course Title	Credits	Letter Grade	Grade Point	Month & Year of Passing
I	18EC101	ENGINEERING MATHEMATICS I	4	B+	7	MAR 2022
I	18EC102	ENGINEERING PHYSICS I	3	B+	7	MAR 2022
I	18EC103	ENGINEERING CHEMISTRY I	3	A	8	AUG 2022
I	18EC104	COMPUTER PROGRAMMING I	3	B+	7	MAR 2022
I	18HS101	COMMUNICATIVE ENGLISH I	2	O	10	MAR 2022
I	18EC106	ENGINEERING GRAPHICS	3	B+	7	MAR 2022
II	18EC201	ENGINEERING MATHEMATICS II	4	B	6	JUL 2022
II	18EC202	ENGINEERING PHYSICS II	3	A	8	JUL 2022
II	18EC203	ENGINEERING CHEMISTRY II	3	B+	7	JUL 2022
II	18EC204	CIRCUIT ANALYSIS	4	C	5	MAY 2023
II	18EC206	ANALOG ELECTRONICS I	3	A	8	JUL 2022
II	18EC207	COMPUTER PROGRAMMING II	3	B+	7	JUL 2022
II	18HS201	LANGUAGE ELECTIVE: COMMUNICATIVE ENGLISH II	2	A	8	JUL 2022
II	18EC208	BASIC ELECTRONICS LABORATORY	1	A+	9	JUL 2022
III	18EC301	ENGINEERING MATHEMATICS III	4	B+	7	DEC 2022
III	18EC302	BASICS OF ELECTRICAL ENGINEERING	3	B+	7	DEC 2022
III	18EC303	DIGITAL LOGIC CIRCUIT DESIGN	4	B+	7	DEC 2022
III	18EC304	ANALOG ELECTRONICS II	4	B+	7	DEC 2022
III	18EC305	LINEAR INTEGRATED CIRCUITS	3	B+	7	DEC 2022
III	18EC306	DATA STRUCTURES AND ALGORITHMS	3	B+	7	DEC 2022
III	18EC307	ANALOG ELECTRONICS AND IC LABORATORY	1	A	8	DEC 2022
III	18EC308	DIGITAL ELECTRONICS LABORATORY	1	A	8	DEC 2022
III	18GE301	SOFT SKILLS - VERBAL ABILITY	0	Excellent	-	DEC 2022
IV	18EC401	PROBABILITY AND RANDOM PROCESS	4	B+	7	JUN 2023
IV	18EC402	SIGNALS AND SYSTEMS	4	A	8	JUN 2023
IV	18EC403	ANALOG COMMUNICATION	3	A	8	JUN 2023
IV	18EC404	PRINCIPLES OF VLSI DESIGN	3	B+	7	JUN 2023
IV	18EC405	ELECTROMAGNETIC FIELDS AND WAVEGUIDES	4	B+	7	JUN 2023
IV	18EC406	MICROPROCESSORS AND MICROCONTROLLERS	3	A	8	JUN 2023
IV	18EC407	ANALOG COMMUNICATION LABORATORY	1	A+	9	JUN 2023
IV	18EC408	MICROPROCESSORS AND MICROCONTROLLERS LABORATORY	1	O	10	JUN 2023
IV	18GE401	SOFT SKILLS - BUSINESS ENGLISH	0	Good	-	JUN 2023
IV	18HS001	ENVIRONMENTAL SCIENCE	0	Excellent	-	JUN 2023
V	21EC501	DIGITAL COMMUNICATION	4	B+	7	DEC 2023
V	21EC502	DIGITAL SIGNAL PROCESSING	4	A	8	DEC 2023
V	21EC503	TRANSMISSION LINES AND ANTENNAS	4	B+	7	DEC 2023
V	21EC504	CONTROL SYSTEMS	4	A	8	DEC 2023

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Minimum Credits to be Earned for the Award of Degree : 163

The Document is Issued without any correction
Medium of Instruction : English

Date : 08 Jul 2025



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G. S. Srinivasan
Controller of Examinations



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(An Autonomous Institution Affiliated to Anna University, Chennai)

SATHYAMANGALAM - 638 401 TAMIL NADU INDIA

CONSOLIDATED STATEMENT OF GRADES

Folio No.: CA 040023



Name : KANISHMA J	Date of Birth : 20-02-2004	Reg. No. : 7376211EC178
Branch : B.E. ELECTRONICS AND COMMUNICATION ENGINEERING		Period of Study : 2021-2025

Sem.	Course Code	Course Title	Credits	Letter Grade	Grade Point	Month & Year of Passing
Page 1 Continued..						
V	21EC006	PROF. ELEC. I - VIRTUAL INSTRUMENTATION IN EMBEDDED SYSTEMS	3	B+	7	DEC 2023
V	21EC021	PROF. ELEC. II - DIGITAL IMAGE PROCESSING	3	A	8	DEC 2023
V	21EC507	DIGITAL SIGNAL PROCESSING LABORATORY	1	A	8	DEC 2023
V	21EC508	DIGITAL COMMUNICATION AND ANTENNAS LABORATORY	1	A+	9	DEC 2023
V	18GE501	SOFT SKILLS - APTITUDE I	0	Excellent	-	DEC 2023
VI	21HS002	HUMAN VALUES AND ETHICS	2	B+	7	JUN 2024
VI	21EC602	COMPUTER NETWORKS AND PROTOCOLS	3	B+	7	JUN 2024
VI	21EC603	DIGITAL VLSI SYSTEM DESIGN	4	A	8	JUN 2024
VI	21EC015	PROF. ELEC. III - ASIC DESIGN	3	B	6	JUN 2024
VI	21EC039	PROF. ELEC. IV - MACHINE LEARNING TECHNIQUES	3	B	6	JUN 2024
VI	21OGE02	PROF. ELEC. V - ENTREPRENEURSHIP DEVELOPMENT I	3	B+	7	JUN 2024
VI	21EC607	COMPUTER NETWORKS LABORATORY	1	A	8	JUN 2024
VI	21EC608	VLSI DESIGN LABORATORY	1	A	8	JUN 2024
VI	18GE601	SOFT SKILLS - APTITUDE II	0	Good	-	JUN 2024
VII	21EC701	RF AND MICROWAVE ENGINEERING	3	B+	7	NOV 2024
VII	21EC702	WIRELESS COMMUNICATION	4	B	6	NOV 2024
VII	18EC1XD	PROF. ELEC. VI - HARDWARE DESIGN USING VERILOG HDL	1	B	6	JUN 2024
VII	18EC1XE	PROF. ELEC. VI - MACHINE LEARNING FOR INTELLIGENT ROBOTS	1	B+	7	JUN 2024
VII	18EC1XC	PROF. ELEC. VI - DATA COMMUNICATIONS PROTOCOLS IN INDUSTRIAL NETWORKS	1	A+	9	DEC 2023
VII	21EC041	PROF. ELEC. VII - DEEP LEARNING TECHNIQUES	3	B+	7	NOV 2024
VII	21EC044	PROF. ELEC. VIII - CONSUMER ELECTRONICS	3	B+	7	NOV 2024
VII	21EC003	PROF. ELEC. IX - EMBEDDED C PROGRAMMING	3	B+	7	JUN 2024
VII	21EC707	HIGH FREQUENCY COMMUNICATION LABORATORY	1	A	8	NOV 2024
VII	21EC708	PROJECT WORK I	3	A+	9	NOV 2024
VIII	21EC801	PROJECT WORK II	9	A+	9	APR 2025

*** End of Statement ***

Class Obtained : FIRST CLASS

Cumulative Grade Point Average : 7.37

PROF. ELEC. indicates PROFESSIONAL ELECTIVE

Minimum Credits to be Earned for the Award of Degree : 163

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Date : 08 Jul 2025



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G. Sathyanarayanan

Controller of Examinations